HZB BESSY II Light Source

Bumpless Transition to New Orbit Feedback in BESSY II and MLS using Libera Spark ERXR

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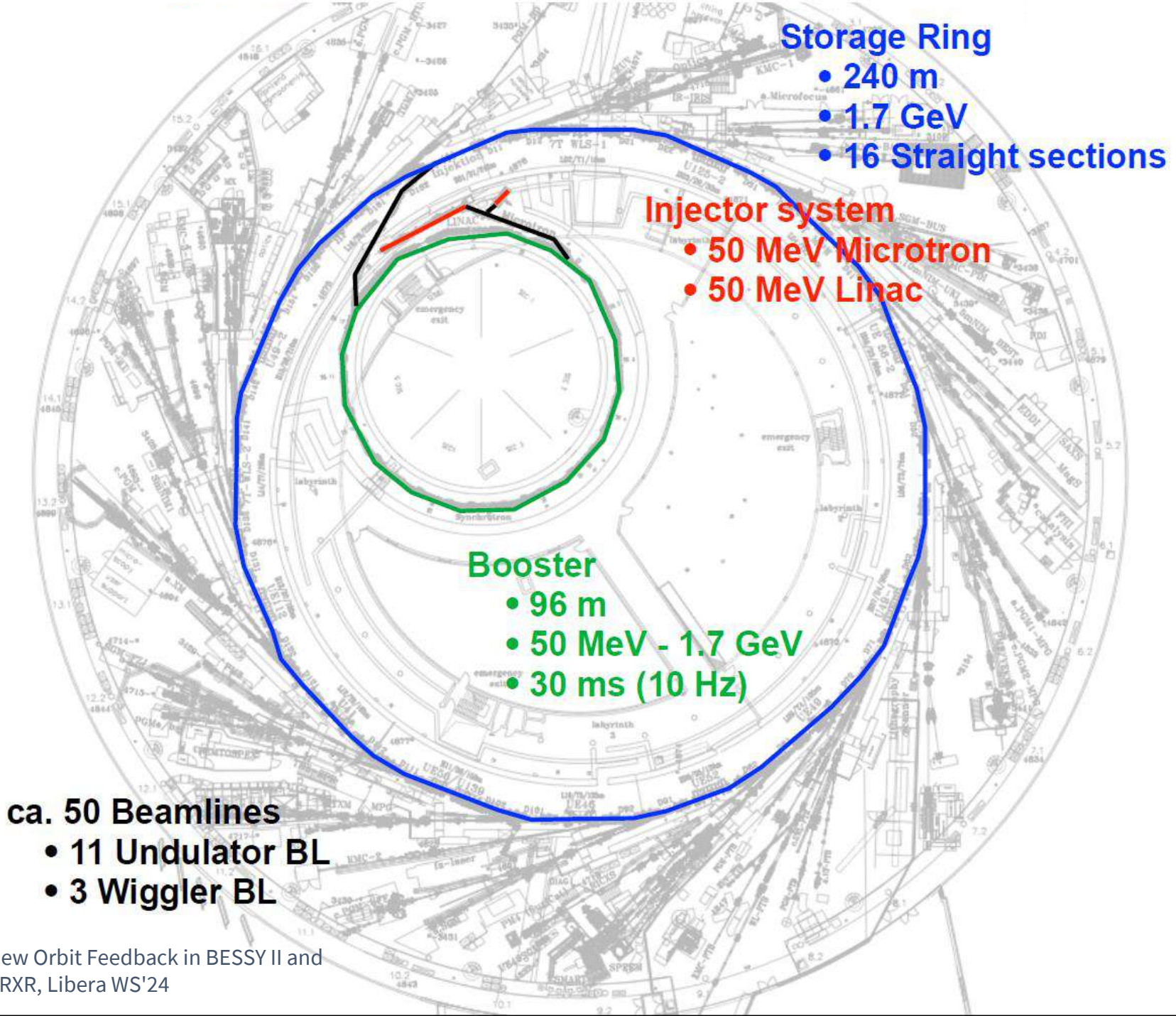
New eyes for mature machines

BESSY II

In operation
since 1998
$\epsilon_x = 5$ nm rad
$\epsilon_y \approx 50$ pm rad



EXISTING BPM ELECTRONICS

- **Original BESSY II BPMs:**
  - Single channel multiplexing
  - Analogue processing of X,Y and S
  - 1kHz analogue output bandwidth
  - Digitisation in VME, EPICSs PVs output at 0.5Hz rate
- **Issues:**
  - LO drift or poor amplitude, no spare parts
  - TbT not usuable
  - Power supply failures
  - Other old age electronics issues

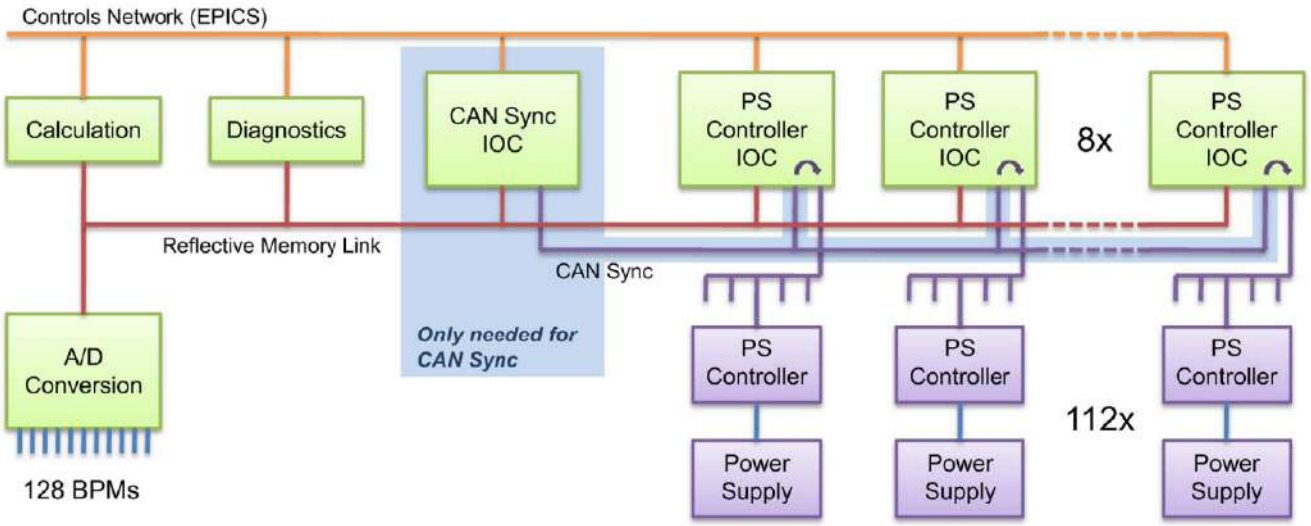
BPM-HF and BPM-AD, mid 1990s in-house design and build

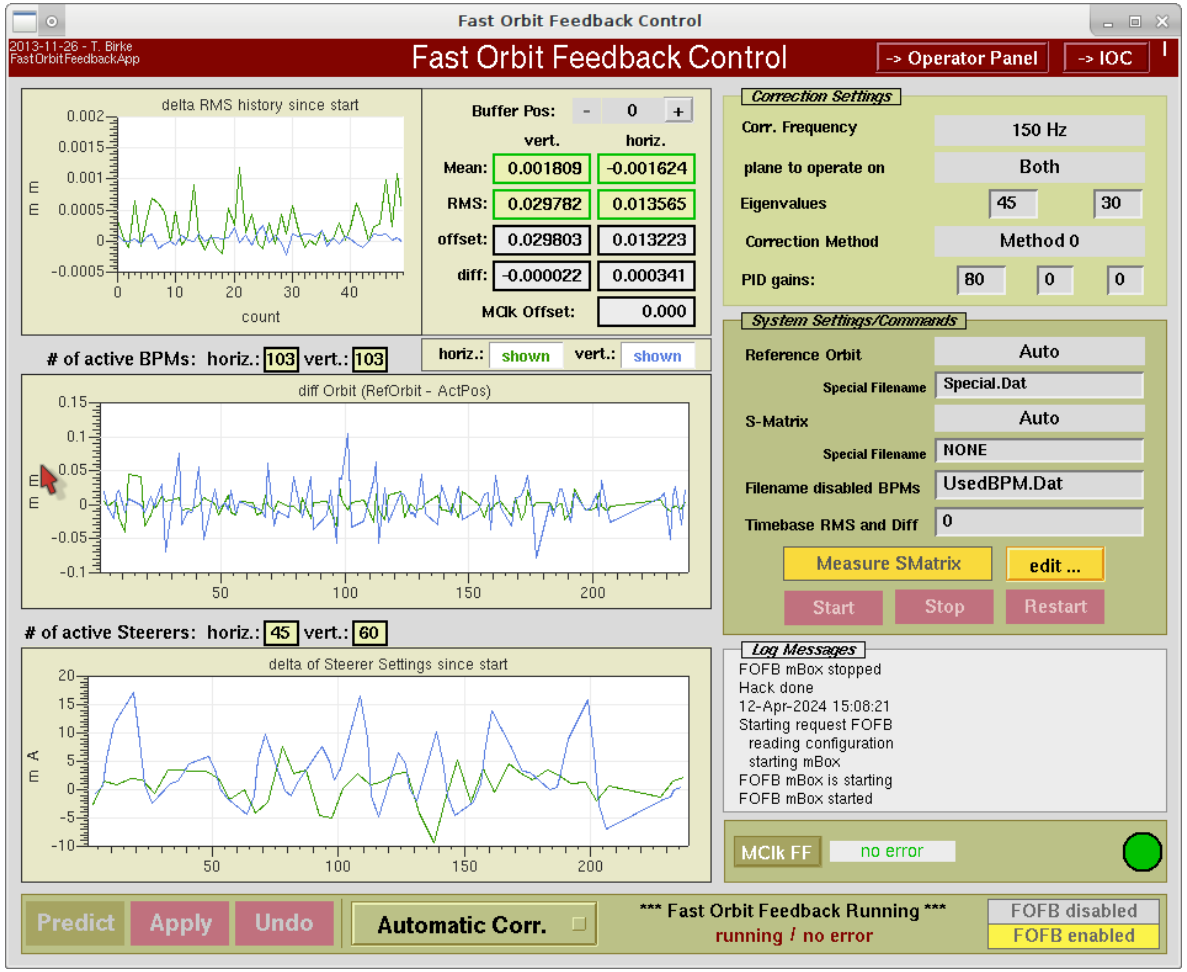
EXISTING SLOW ORBIT FEEDBACK

- EPICS to BPMs and corrector PSUs
- Corrections at 0.5 Hz
- In operation since dawn of BESSY II
- Monolithic Tcl/Tk tool
- Disabling of individual BPMs and correctors
- Used during startup and maintenance
- **Continue use during transition**
- **Replace after transition**

EXISTING FAST ORBIT FEEDBACK

- In operation since 2012
- Runs at 150 Hz update rate, limited by large latency
- Uses reflective memory to distribute BPM data and corrector data
- Bottleneck is large latency in CAN bus (>3ms)
- **Continue use during transition**
- **Replace after transition**

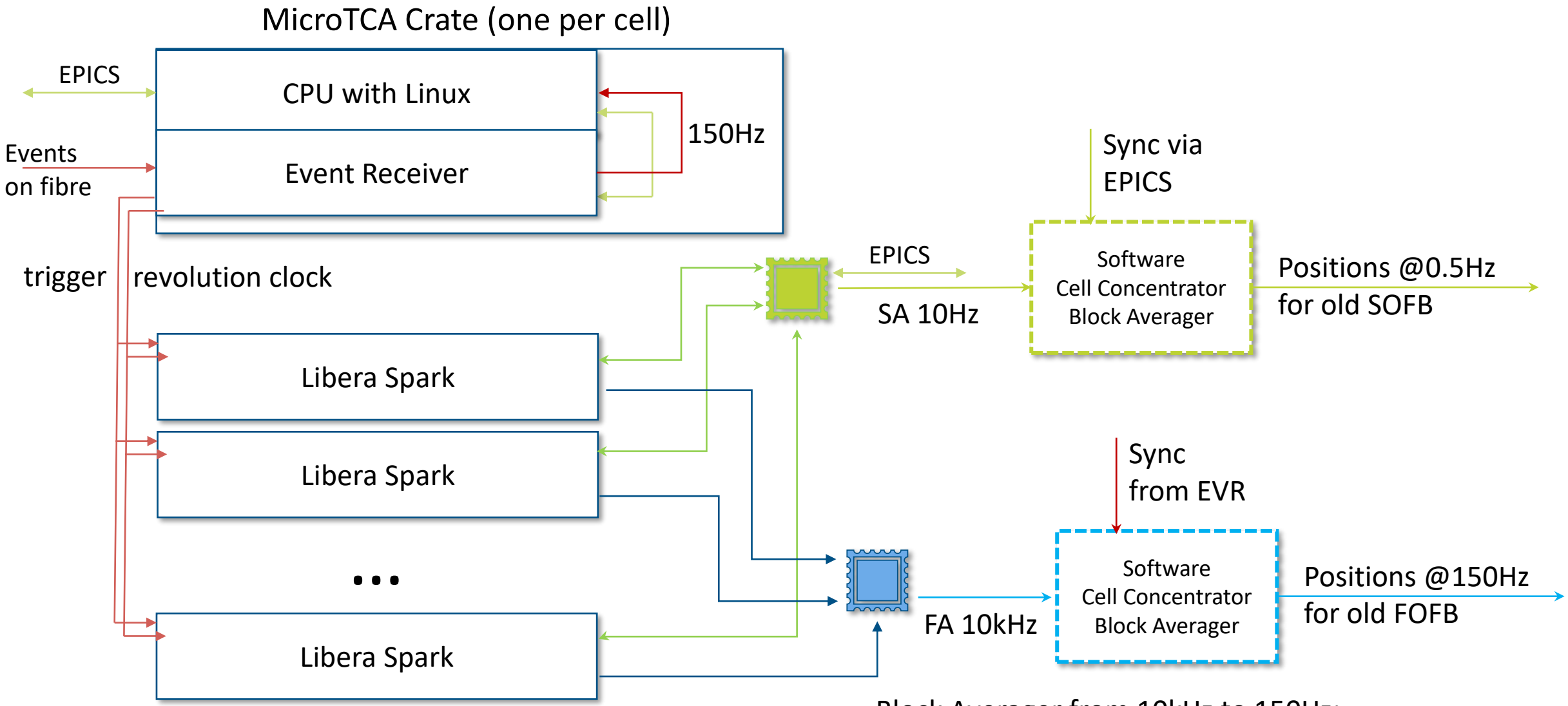




BPM and Orbit Feedback Upgrade Plan

1. Install new BPMs cell by cell
   - Operate SOFB (0.5Hz) and FOFB (150Hz) with hybrid BPM population
   - Need to produce relevant data at original rates and in sync
2. Replace old SOFB with new Python routine
   - Headless server, communicates with users solely through EPICS
   - Will also be able to communicate with new FOFB
3. Replace old FOFB with new routine using FA data at full rate
   - Transfer to compuational node through GBE and 10 GBE
   - GBE Receiver for cell corrector values produces DAC values for PSU

TIMING OF NEW BPMS AND HYBRID ORBIT FEEDBACK (PLAN 2023)



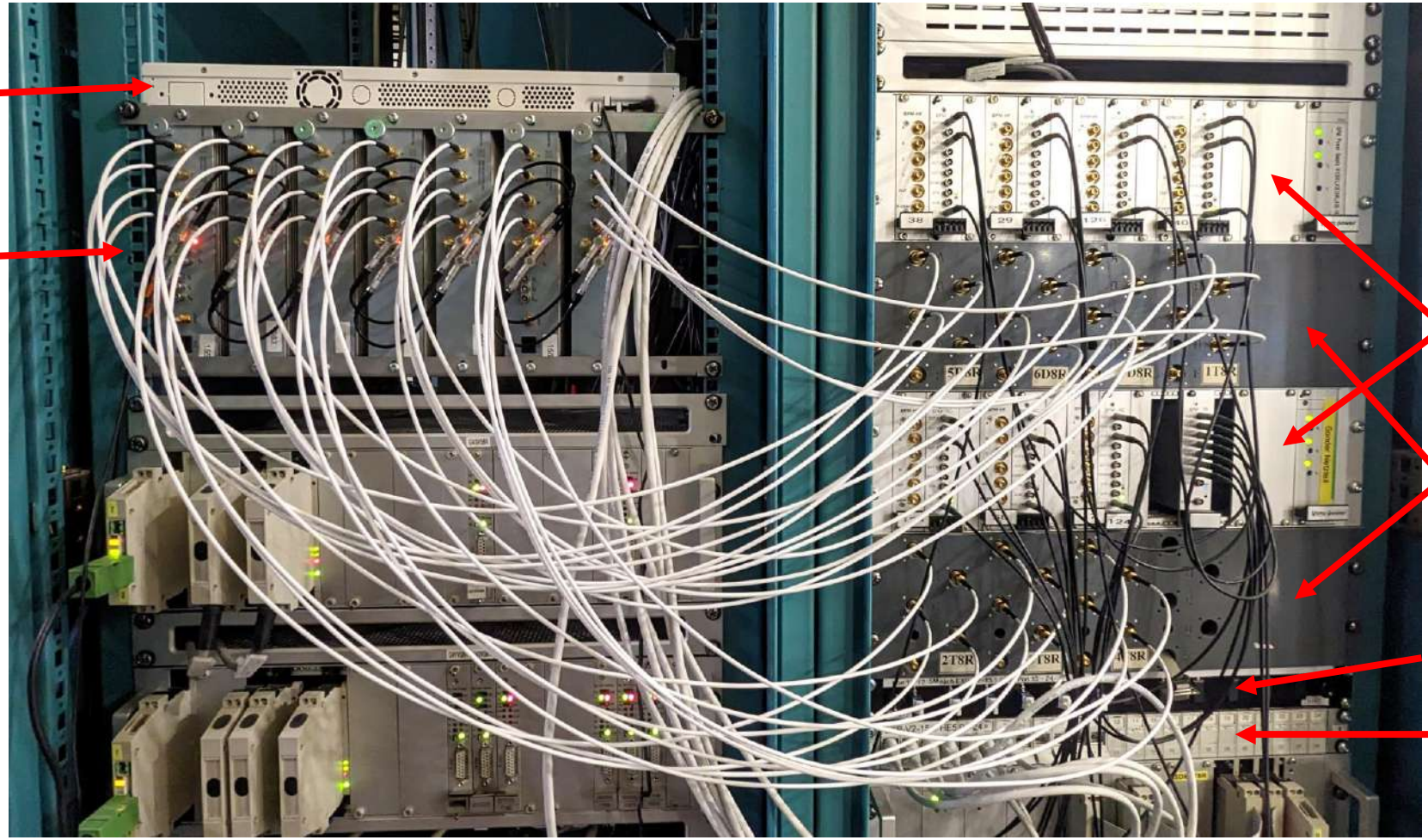
Block Averager from 10kHz to 150Hz:

- Software in MTCA CPU
- Firmware in Libera Spark

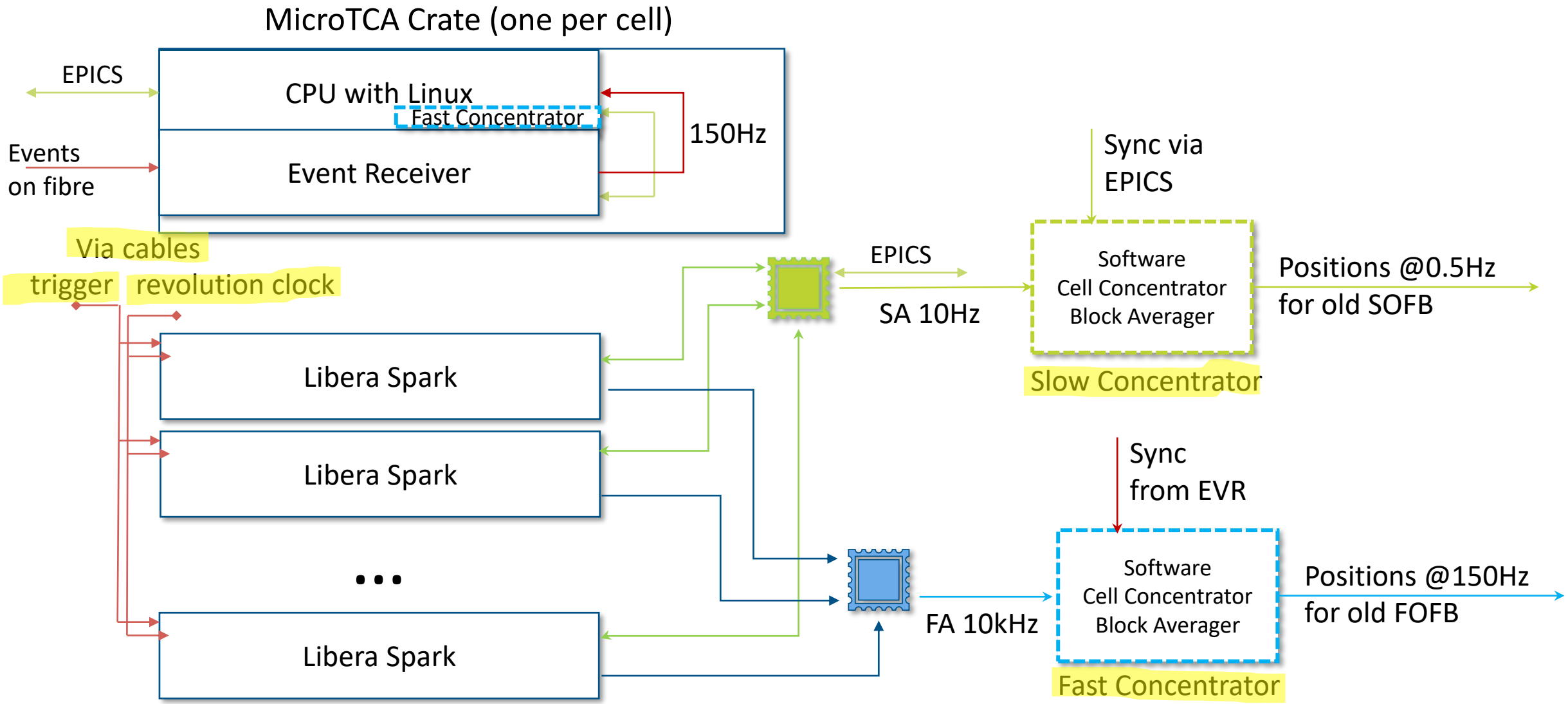
DECEMBER 2023: DELIVERY COMPLETE

- All Spark ERXR units delivered
- All units passed quick in-house test
- Begin to group units for assembly in cells
- Route MC clock and Trigger signals per group
- New features in 2023 vintage of Spark ERXR:
  - Additional 0/5/10/15dB attenuator in RF chain
  - Ability to supply sample clock externally
  - Firmware/Software 2.0,
  - Support of Neon FPU in Arm Processor
  - EPICS IocStats included
  - Pre-installed ssh-key in authorized_keys

FEBRUARY 2024: FIRST TEST IN ONE CELL



TIMING OF NEW BPMS AND HYBRID ORBIT FEEDBACK (CURRENT)

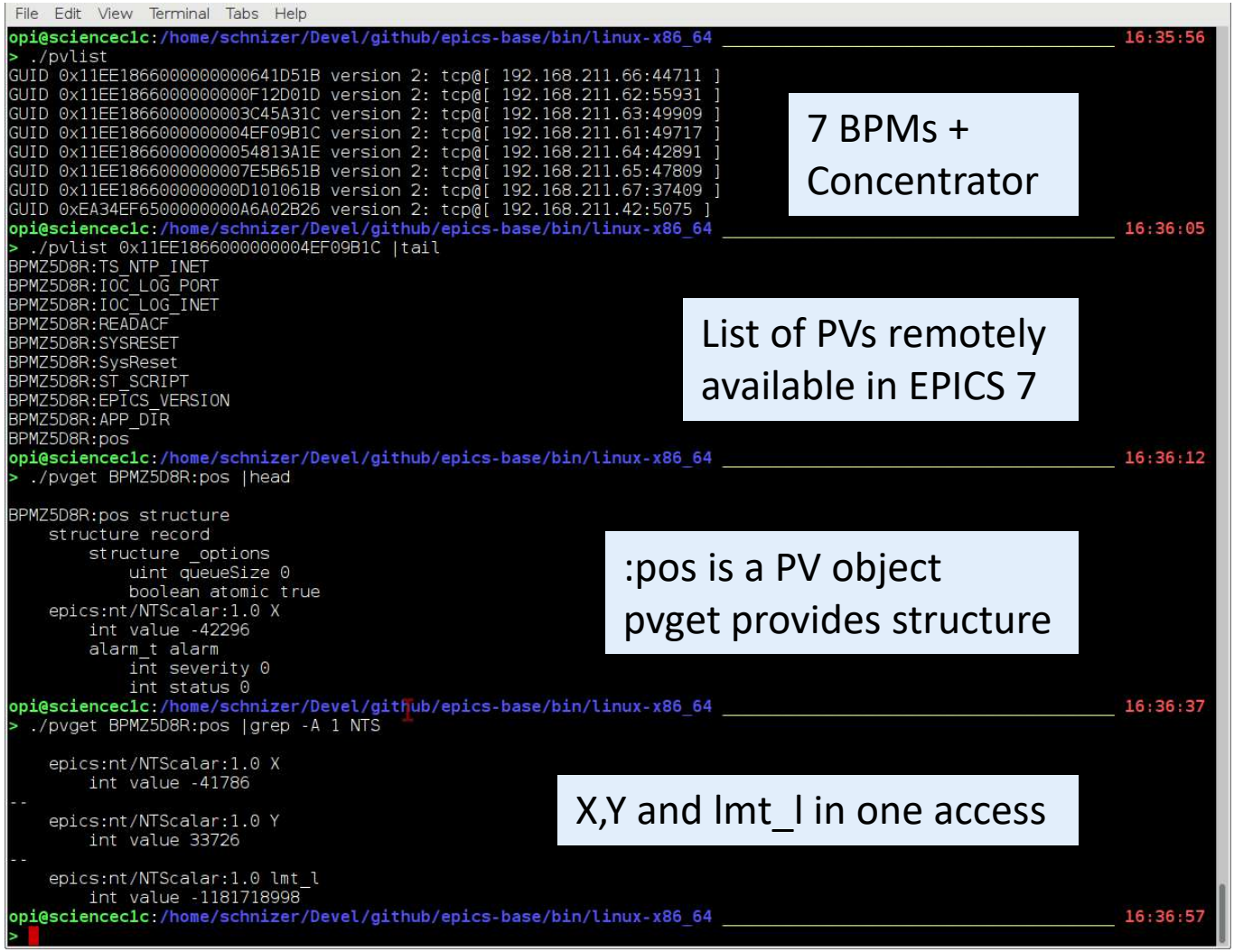


Block Averager from 10kHz to 150Hz:

- Software in MTCA CPU

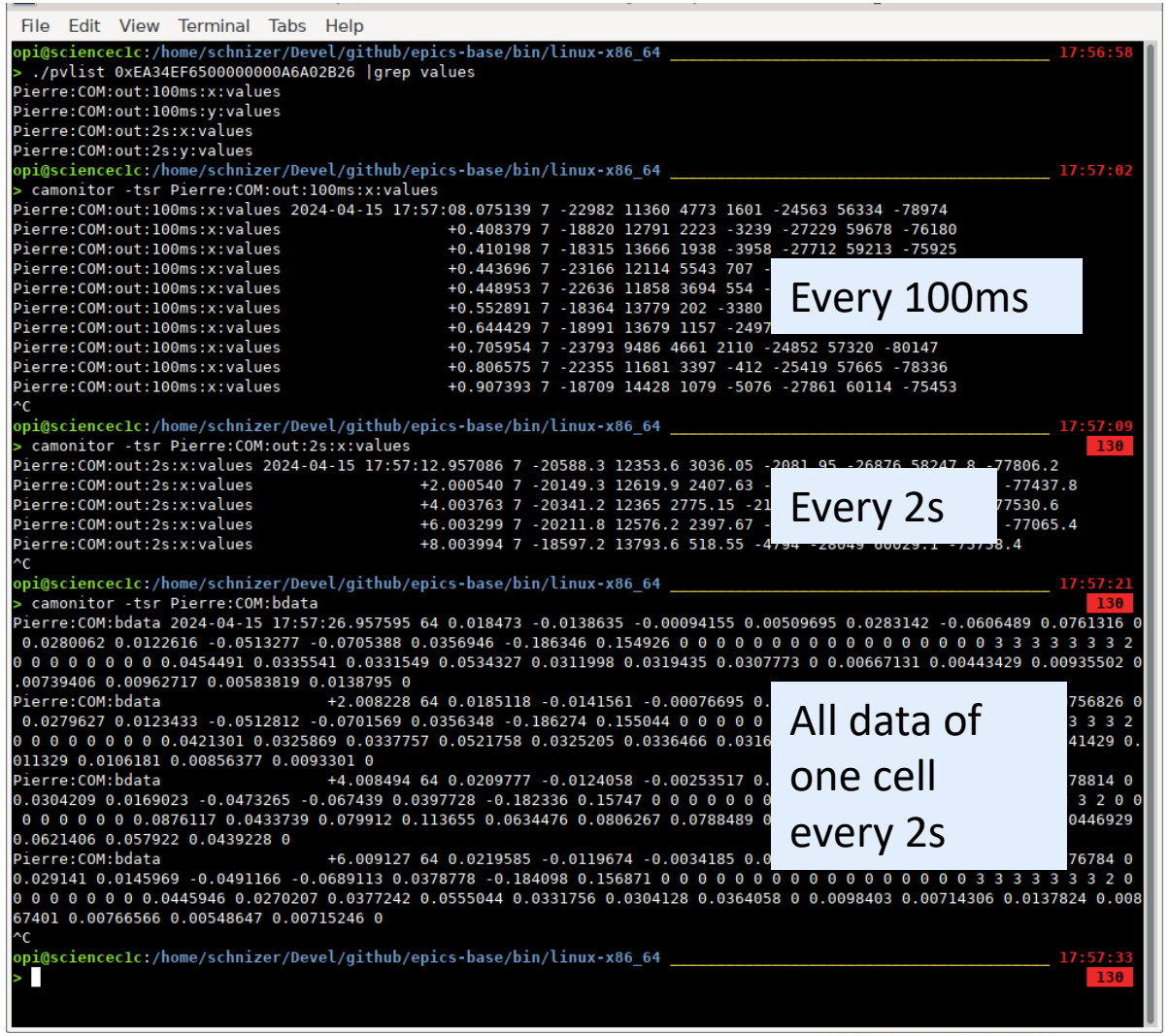
MARCH 2024: SLOW CONCENTRATOR USING EPICS7 (1)

- Instrumentation Technologies offer EPICS3 and EPICS7
- EPICS7 is missing „qsrv“
- Required for construction of PV objects
  - ➢ Compile EPICS7 IOC at HZB using supplied virtual machine
- Ammendment of database file on Spark
  - ➢ Now PV object provides X,Y, timestamp in **one access**
- Software changes roled out through DHCP, TFTP, NFS



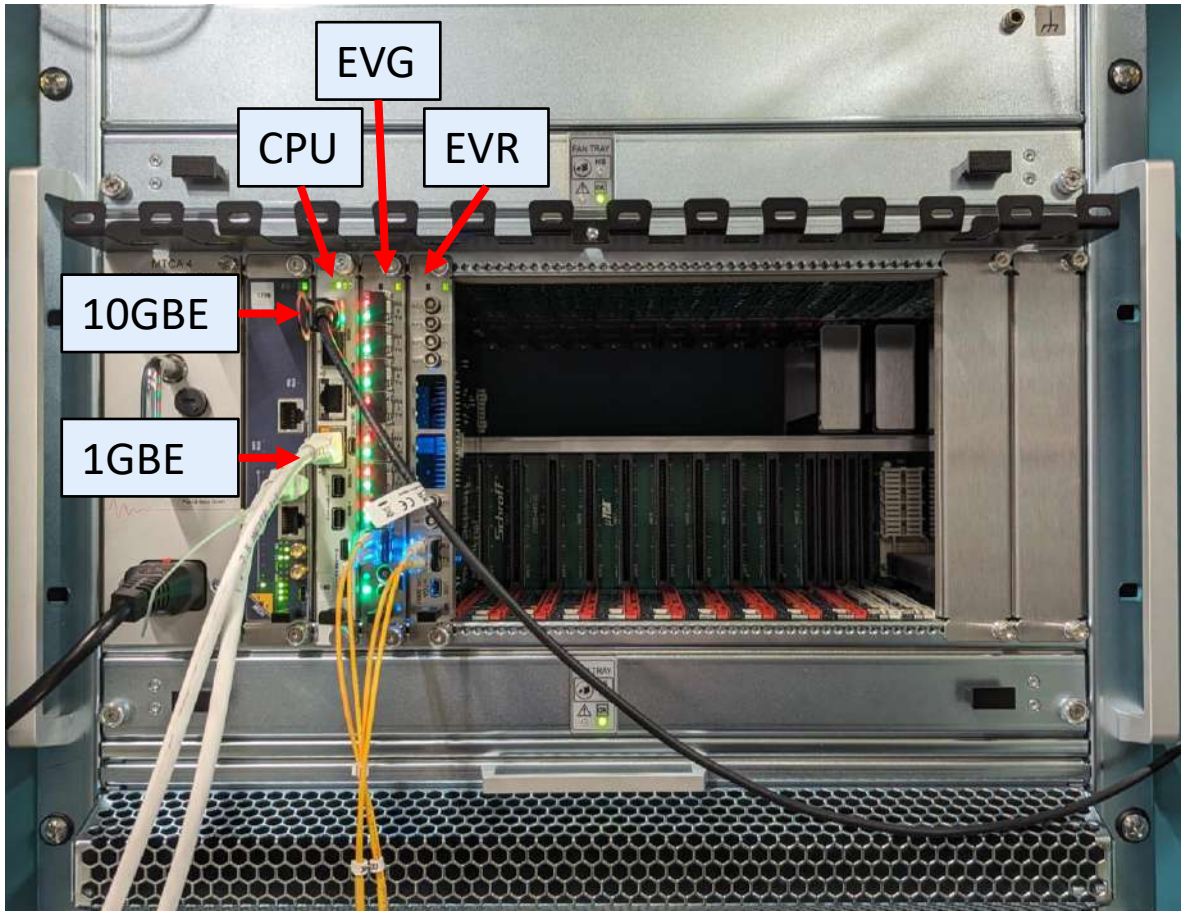
MARCH 2024: SLOW CONCENTRATOR USING EPICS7 (2)

- Slow Concentrator produces collected values at two rates: every 100ms and every 2s
- Format of previous hard IOC is reproduced to replace one cell
- „bdata“ contains all information from one cell



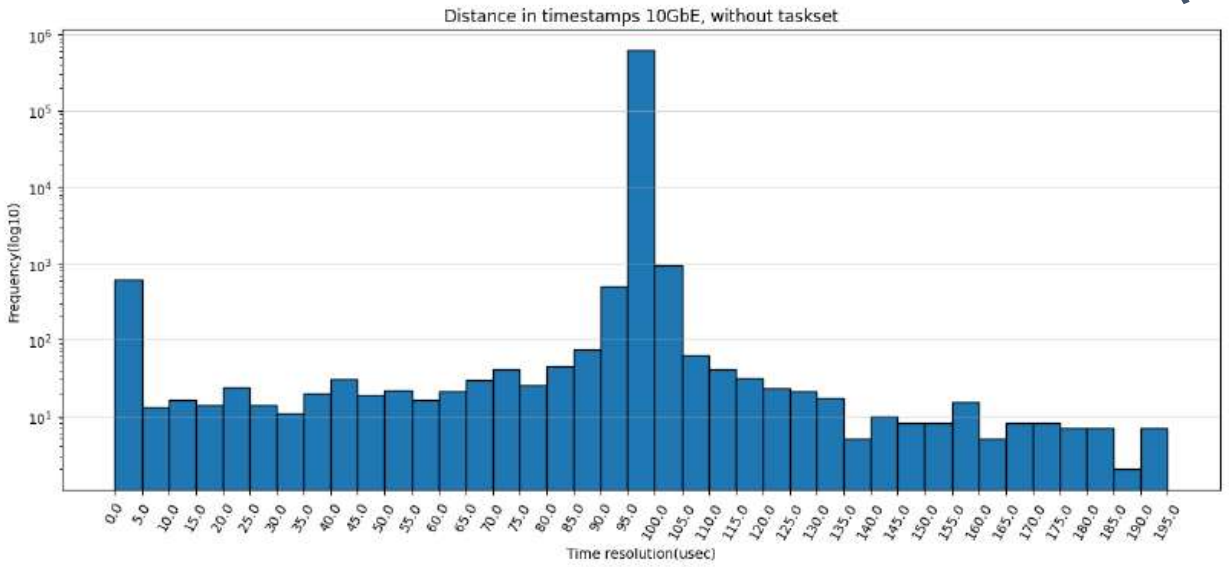
APRIL 2024: FAST CONCENTRATOR USING LOW-LATENCY TUNED LINUX (1)

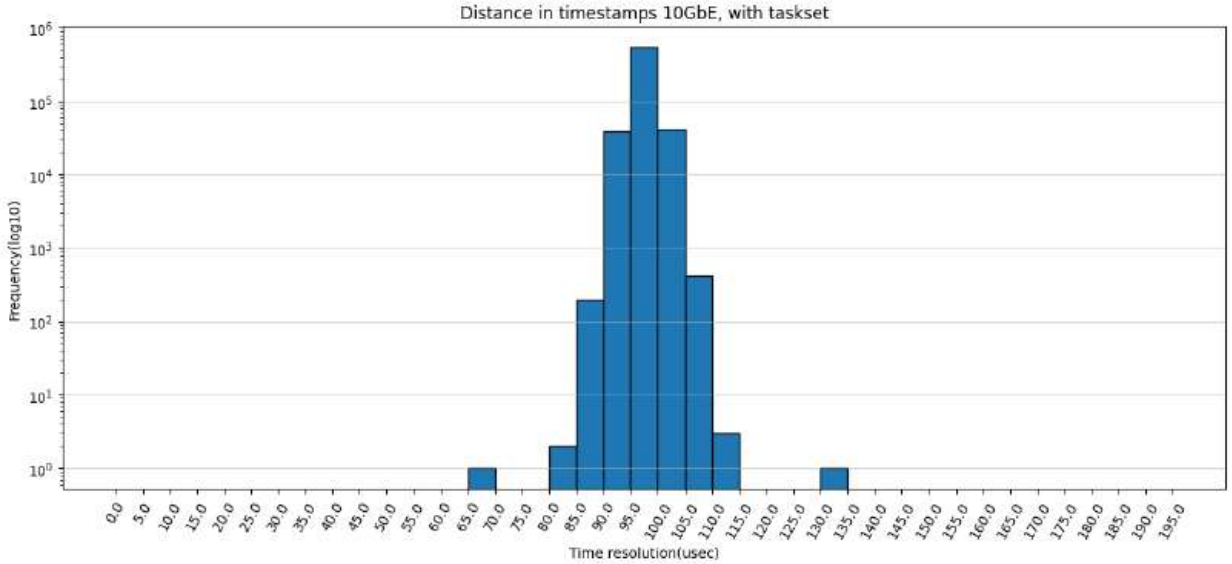
- MTCA crate, power supply, MCH
- AMC-CPU with 4 core Xeon, MRF-EVG/EVR
- One cell of UDP FA packets received in CPU
- Incoming data on 10GBE
- Outgoing temporarily on 1GBE, parallel with EPICS
- EVG (temporarily here) creates 150Hz event rate
- EVR receives event, fires PCIe interrupt on CPU
- On interrupt, all received FA packets are averaged
- New UDP packet sent in compressed format forward towards FOFB compute node
- Also to be replicated in all 16 cells, but only one EVG



APRIL 2024: FAST CONCENTRATOR USING LOW-LATENCY TUNED LINUX (2)

- Standard Linux tuned using mostly using run-time configuration
- Worked well enough on Ubuntu, Fedora, Debian and various kernels
- Verified by timestamping using monotonic clock
- No significant impact of 24port GBE Switch and 10GBE output

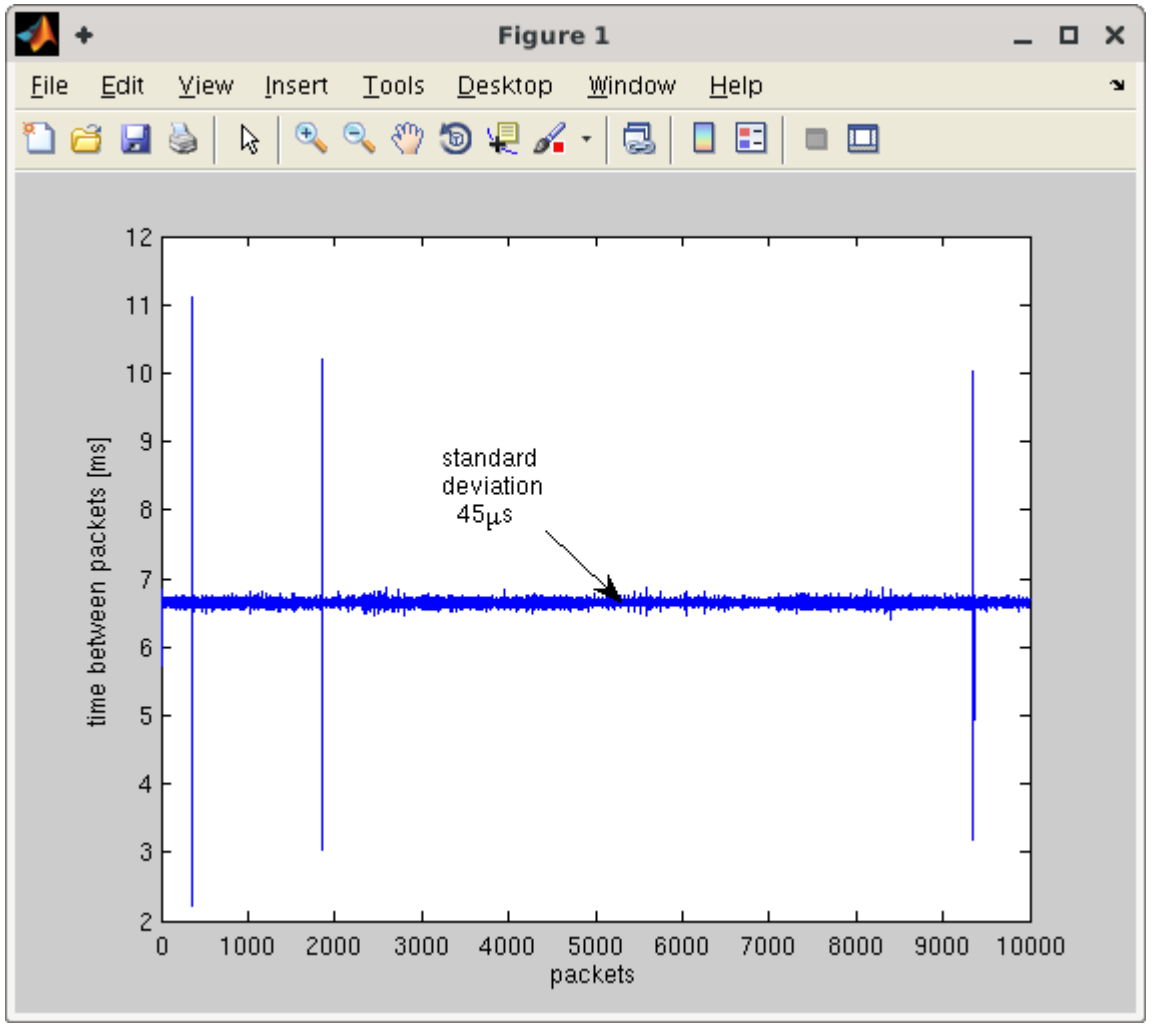




APRIL 2024: RECEIPTION OF COMPRESSED UDP PACKETS IN MATLAB

- Current FOFB application runs in MATLAB
- Reception using MEX-file written in C
- Tested time between received UDP packets
- Acceptable standard deviation found
- Occasional glitches need to be addressed
  - ➢ Linux Low-Latency tuning required!

- Next steps:
  - ➢ Share orbit trigger between old and new BPMs
  - ➢ Integrate UPD and RFM receiption in FOFB



Summary

Spark ERXR with custom modifications received

First Cell replaced

Integrated on SA rate through EPICS

Integrated on FA rate using software averager

UDP packets received successfully in MATLAB

Thank you for your attention!

Questions?