first measurements with Libera Spark

collaboration between ESRF and I-Tech in development of Libera Spark

Motivation: replacement of the 25 years old ESRF's **Booster BPM system (electronics)**

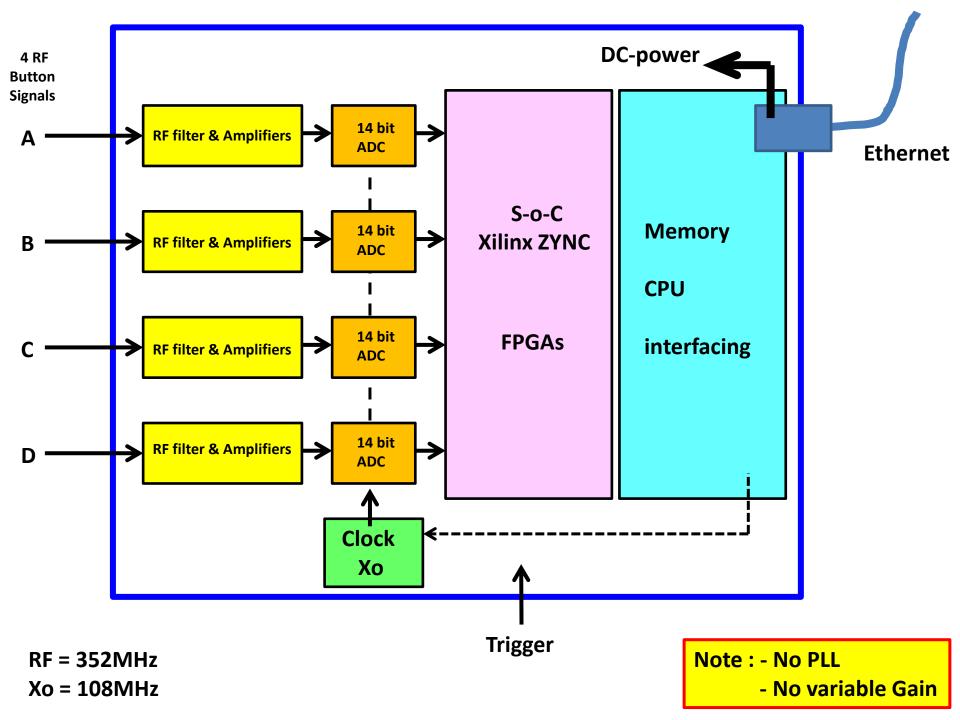
Goals & Aims : modern system with maximum sensitivity & lowest noise capable of Turn-by-Turn measurements

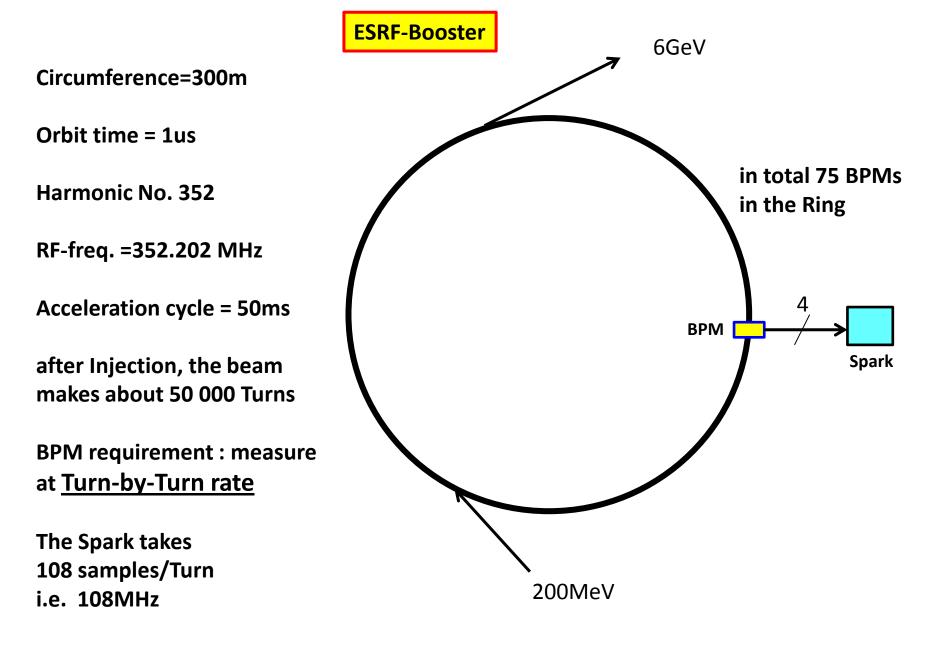
Other considerations :

- simplicity
 - low costs
 - self-contained system

The Spark, product definition : - 4 channel digitizer for weak RF signals

- - adequate signal processing for data-rate reduction
 - efficient interface for control & read-out via Ethernet
 - suitable chassis & housing, with Power-over-Ethernet

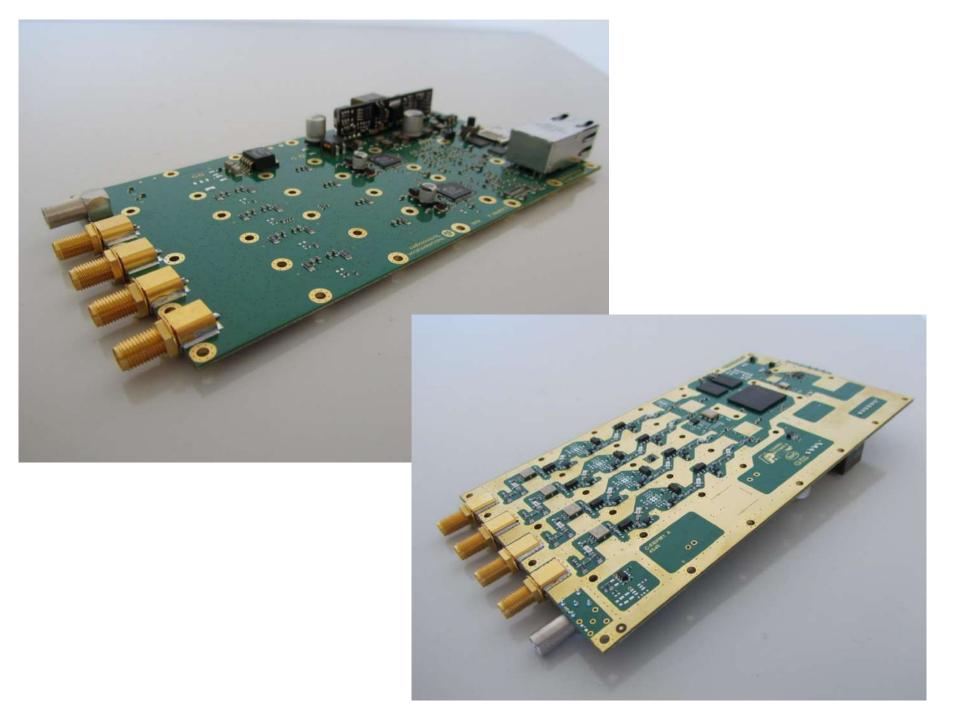


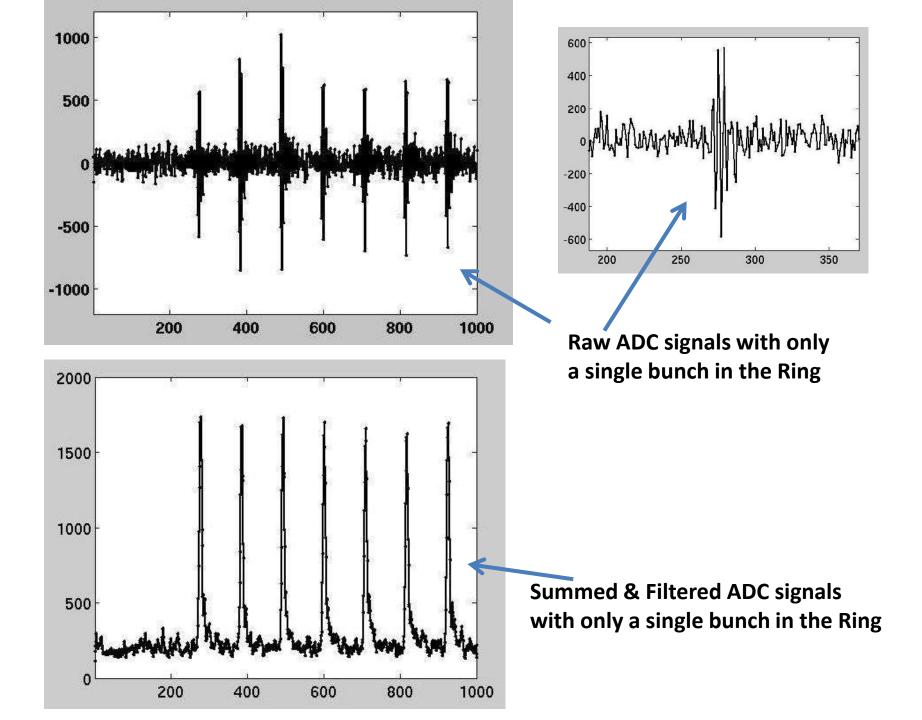


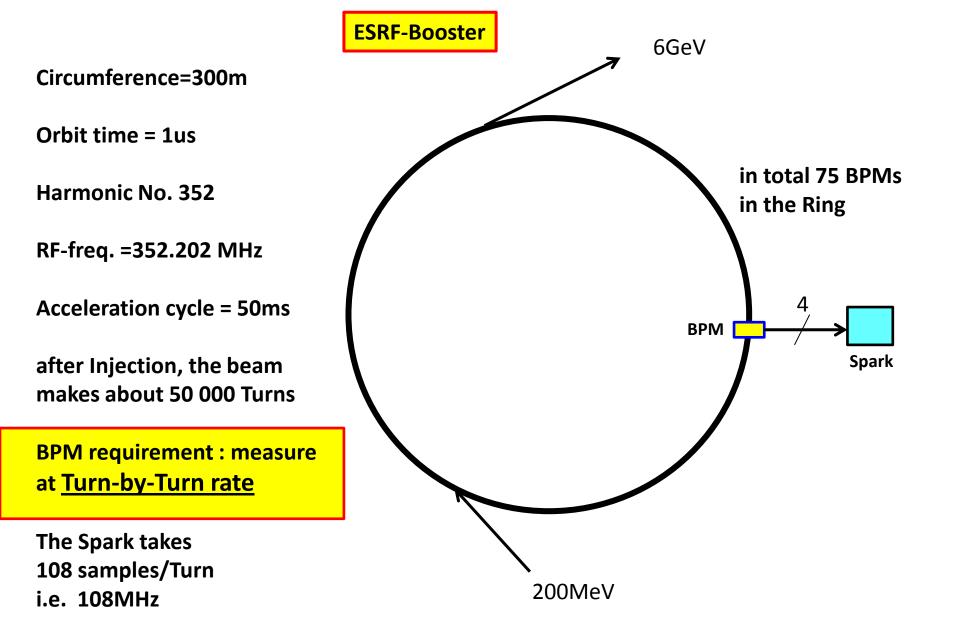
Libera-Spark

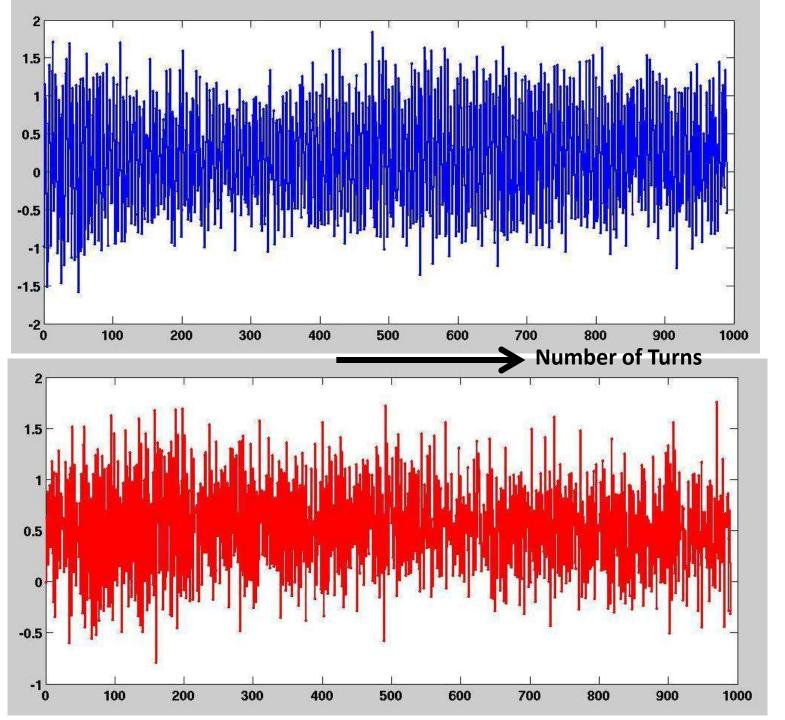


Libera-Spark



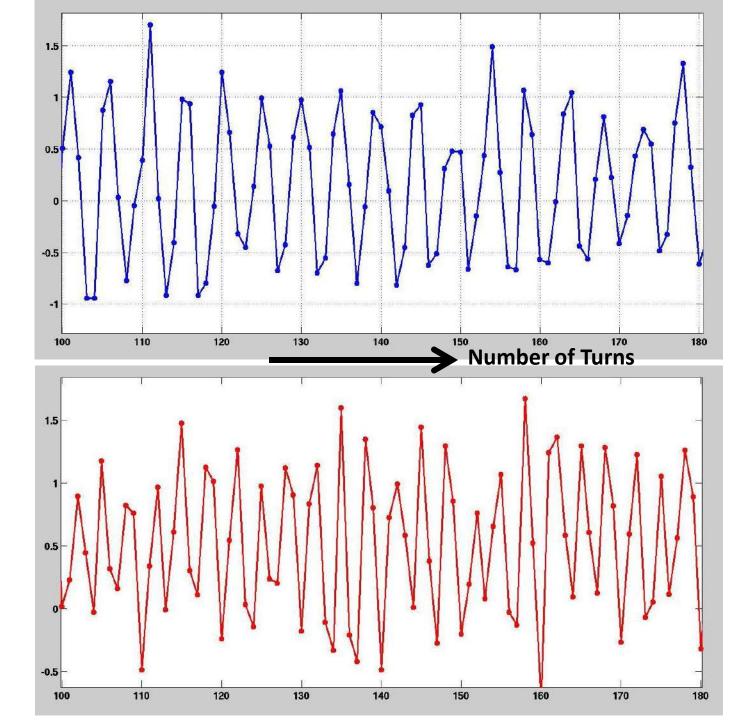






Hor. Position

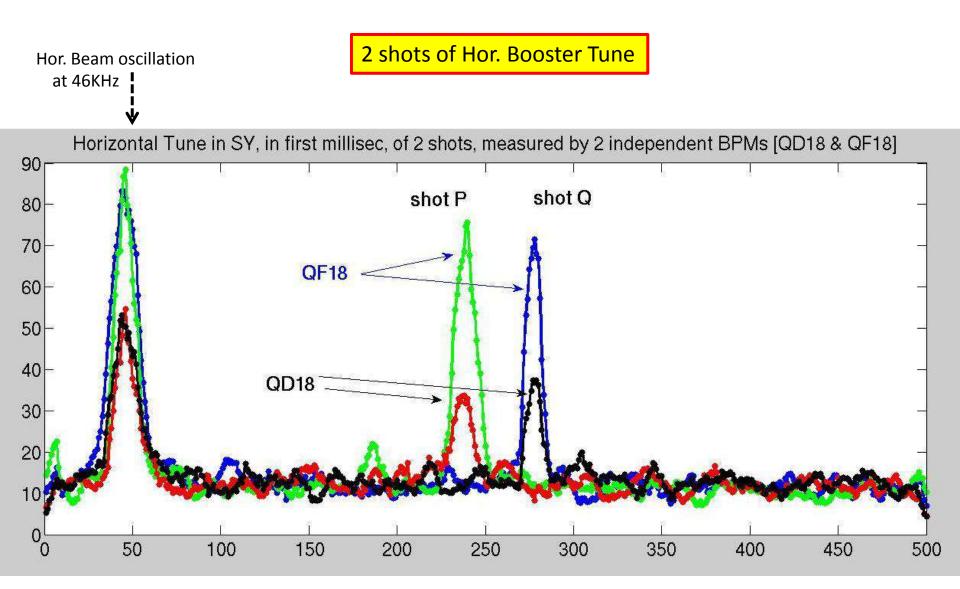
Vert. Position

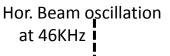




Time-Domain Processing on the ADCs

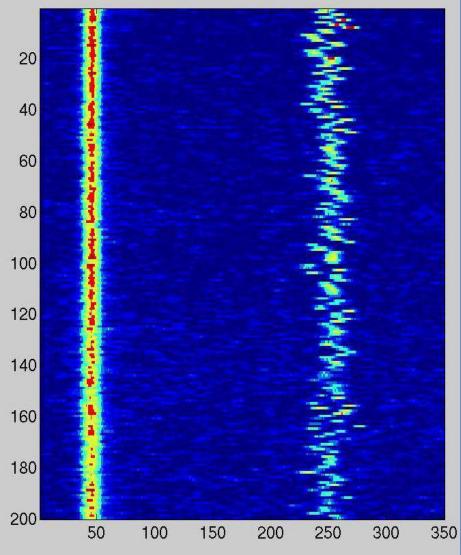
Vert. Position 24 oscillations

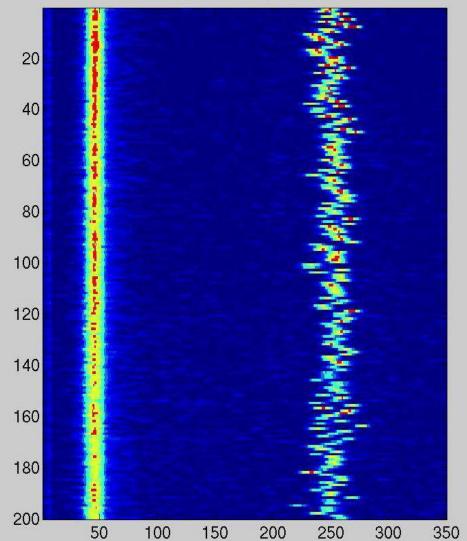




200 shots of Hor. Booster Tune, showing strong fluctuations

Horizontal Tune in SY, in first millisec, from QD18, over 200 shots



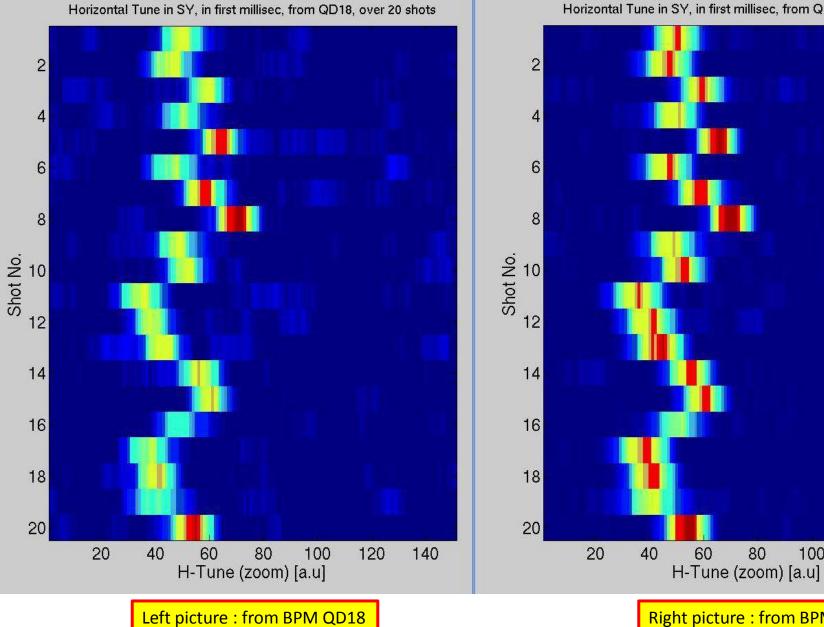


Horizontal Tune in SY, in first millisec, from QF18, over 200 shots

Left picture : from BPM QD18

Right picture : from BPM QF18

20 shots of Hor. Booster Tune, showing strong fluctuations



Horizontal Tune in SY, in first millisec, from QF18, over 20 shots

Right picture : from BPM QF18

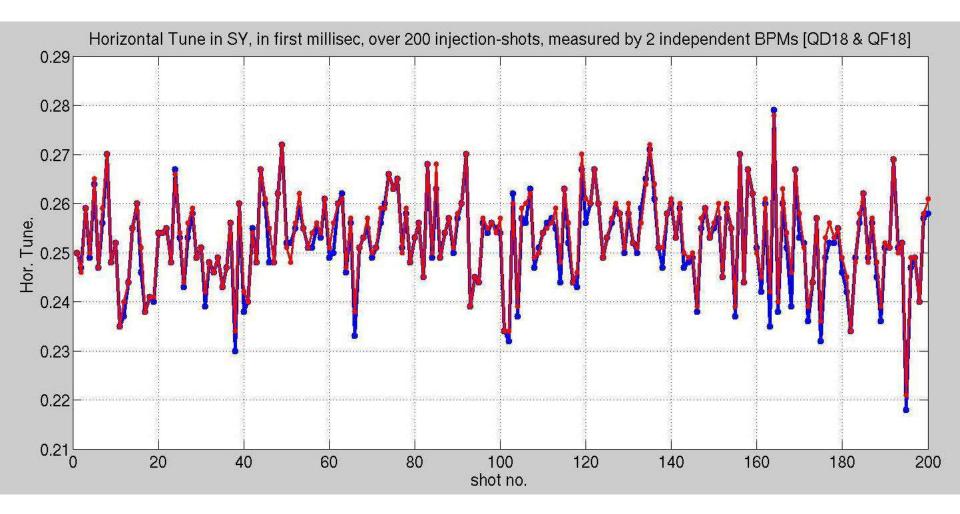
80

100

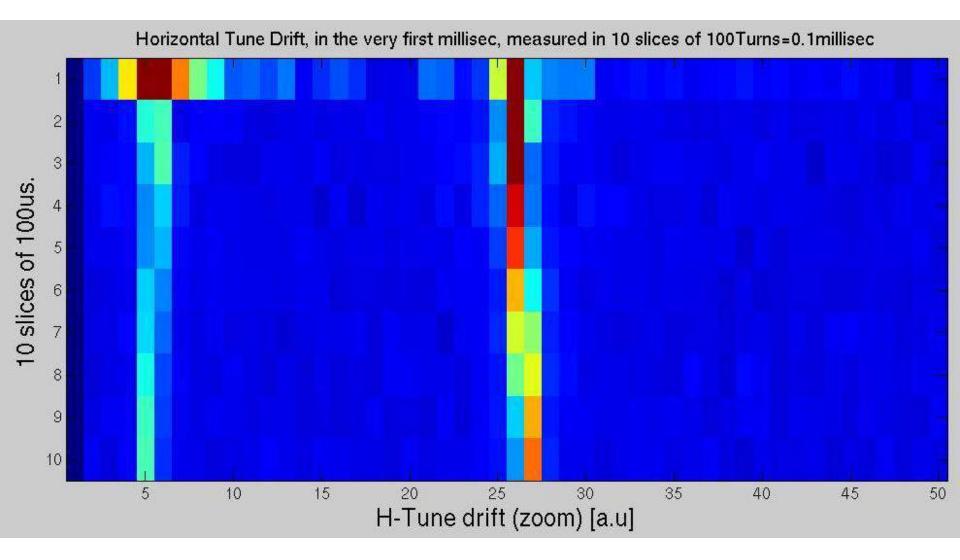
120

140

200 shots of Hor. Booster Tune, Red & Blue curves from 2 independent BPMs



Horizontal Tune drift in the very first millisec after injection



first measurements with Libera Spark

conclusion :

maximum sensitivity & lowest noise

clean & pure Turn-by-Turn measurements

Clever & Optimized Treatment of the signals is required

Other considerations :

- simplicity
- low costs
- self-contained system

OK

The Spark :

- 4 channel digitizer for weak RF signals
 - adequate signal processing for data-rate reduction
 - efficient interface for control & read-out via Ethernet
 - suitable chassis & housing, with Power-over-Ethernet

ОК