

HADRON BPM FOR THE FAIR PROJECT

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Abstract

The accelerators of the Facility for Anti-proton and Ion Research are designed to deliver stable and rare isotope beams covering a huge range of intensities and beam energies. FAIR will employ heavy ion synchrotrons for highest intensities, anti-proton and rare isotope production stations, high resolution separators and several storage rings where beam cooling can be applied. Instrumentation Technologies will develop and deliver a beam diagnostic system for SIS-100, HESR and CR rings. Furthermore the beam transfers will be equipped with the beam position diagnostics. The project is on schedule and the first instrument prototypes are already being under evaluation. This article discusses the new BPM electronics concept, the tests performed in the laboratory and the performance obtained.

INTRODUCTION

Almost ten years ago, twelve Libera BPMs were delivered to GSI. At that time the purpose was building the full diagnostic system for existing SIS-18 accelerator; Instrumentation Technologies provided the hardware with data storage while GSI built advanced Digital Signal Processing.

The UNILAC and the SIS 18-ring of the existing facility (blue) [1] will serve as injectors for the new facilities of the FAIR complex (red), such as the SIS-100 accelerator ring, the HESR storage ring and the various experimental areas (see Fig. 1).

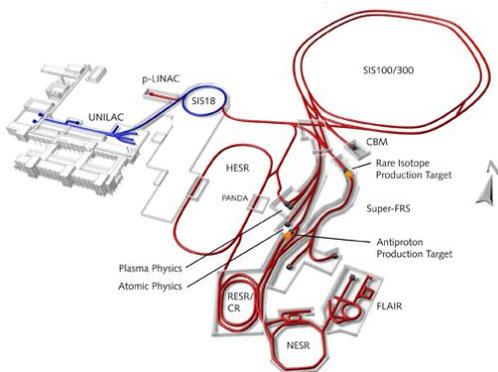


Figure 1: FAIR complex

With FAIR project a new collaboration between GSI - FAIR and Instrumentation Technologies started. The driver for the new Hadron BPM is large SIS-100 synchrotron machine. Below are listed just some of starting points for building the new BPM system for FAIR project. A high accuracy in the order of 100 μm rms of beam position reading is required for the determination of the closed orbit, fast position readout in bunch-by-bunch manner is mandatory especially in cases when

bunch manipulations will be performed, beam position data path has to be available for the closed-orbit feedback system etc.

Successful collaboration between GSI - FAIR and Instrumentation Technologies led to a detailed high level design. Excellent specifications and effective Instrumentation Technologies team, made it possible that in less than one year of hard work the first fully functional prototype of Libera Hadron instrument is available.

LIBERA HADRON

Libera Hadron system (see Fig. 2) is based on uTCA modular technologies with IPMI platform management. The system is therefore developed on multiple AMC modules, with each module covering different functionalities.



Figure 2: Libera Hadron

The user can access the functions implemented in the Libera Hadron unit through a control system interface, called the Measurement and Control Interface (MCI). This interface was developed to facilitate the integration of Libera Hadron into the accelerator's control system software.

DATA PROCESSING

Libera Hadron has a capability of processing signals from shoe-box sensors or capacitive pickups, with various signal intensities, bunch repetition rates and bunch lengths [3]. Three stand alone algorithms are implemented for this purpose:

- Narrow Band analysis: Superheterodyne SDR receiver for processing bunch signals at extremely low bunch charges.
- Bunch-by-bunch for Shoe-box sensors: Bunch detection algorithm with baseline restorer for processing of unipolar signals.
- Bunch-by-bunch for capacitive sensors: Bunch detection algorithm for processing of bipolar pulses.

Libera Hadron provides four main data paths presented on Fig. 3 (raw ADC data, Bunch-by-bunch, Fast Acquisition and Slow Acquisition). Fast Acquisition data is available over SFP connector on the GDX module (optional).

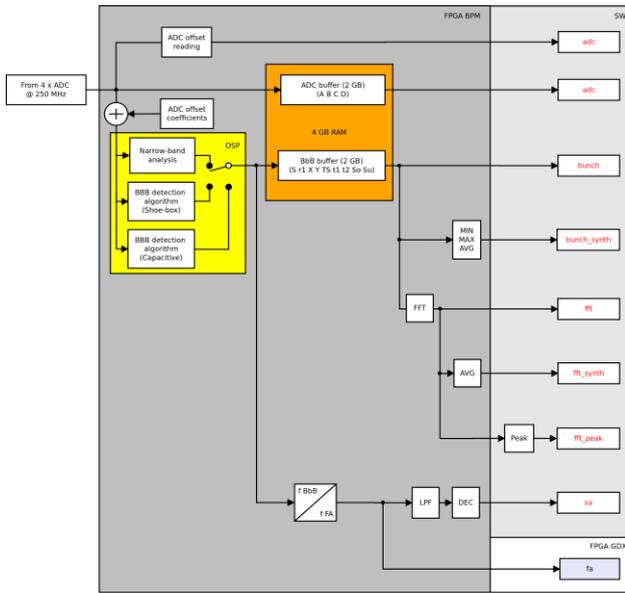


Figure 3: Data paths

Fast Acquisition (10 kHz) and Slow Acquisition (10 Hz) are stream type while other data paths are data on demand type. The complete data processing and data storing in buffers is initiated by the external trigger event signal. The data is stored in two 2 GB buffers which can be read out on users' request:

- ADC buffer (Raw ADC data): data coming directly from the ADC converters @ 250 MHz sampling rate. Ca. 270 M samples (> 1 second of 4 channels data) can be stored. Bunch shapes through time can be observed here.
- BbB buffer (Bunch-by-bunch data): 2 GB of RAM memory reserved for this purpose. Ca. 200 M samples (> 66 seconds of data @ 1 MHz bunch repetition rate) can be stored. The position data is registered for each individual bunch so this data can be used for tune determination.

There are four additional data paths [2], offering to the user various options of data representation and post analysis (Closed orbit, trending, trending mean, tune, FFT, etc). Example of FFT vs. time acquisition is presented on Fig. 4.

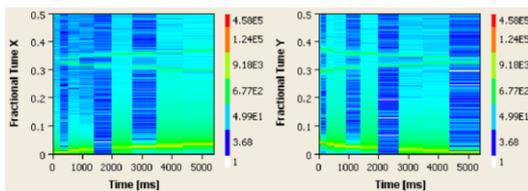


Figure 4: Color coded FFT waterfall diagram vs. time

CONTROL SYSTEM INTEGRATION

On the top layer, Libera Hadron provides the MCI with a development package and Command Line utilities for open interaction in different control systems. On top of the MCI, various adaptors to different control systems can

be implemented (EPICS, Tango, FESA etc.). The EPICS interface is part of the standard software package.

EVENT RECEPTION

Libera Hadron detects the events announced by the accelerator timing system in order to set the data processing parameters, set external preamplifier value or lock to various external signal clocks. It enables event reception via the optics/wire in the event receiver module (EvRx).



Figure 5: Event receiver module.

The EvRx (see Fig. 5) module receives the optical signal through the SFP transceiver and identifies and extracts the 16-bit event code. Once the code has been extracted, the module decodes the event identification code and triggers specific functions at low latency. EvRx module is compatible with Micro Research Finland (MRF) and White Rabbit timing systems.

PROTOTYPE TESTING

Measurement performance mostly depends on the Libera front-end configuration. Its parameters are set in accordance with various accelerator characteristics. The most important are sensor type, beam dynamic range, distance between bunches and bunch length. In the SIS-100 case the typical bunch lengths are 30 ns with maximal repetition rates of 3.3 MHz.

The crucial GSI - FAIR requirement for the RF front end design was flat frequency response in range from 40 kHz up to 55 MHz, considering that -20 dB rejection has to be reached at 125 MHz. The RF front-end frequency response measurement results are presented on Fig. 6. Measurement was performed in the range from 40 kHz to 150 MHz. The signal source was Rohde-Schwarz SMA100A signal generator. Since the generator and test setup are not linear, the test setup was measured with the spectral analyzer prior the test. The measured deviations were considered in the final result (see Fig. 6).

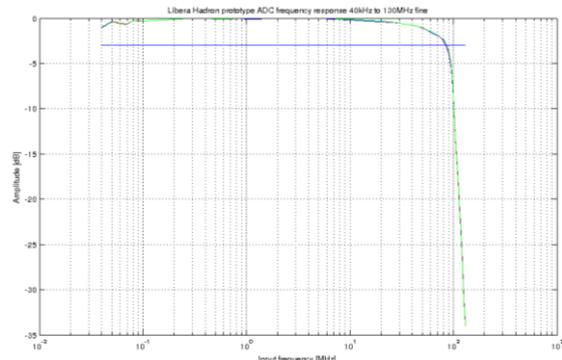


Figure 6: RF front-end frequency response

Position measurement resolution requirement for SIS-100 is 100 μm in bunch-by-bunch mode within the 10 dB dynamic range. The full scale of the instrument must be 2V peak signal.

Position resolution was estimated with four equal signals applied to the Libera Hadron RF inputs. Evaluated signal is presented on Fig. 7. The signal was measured directly at the Libera input. Repetition frequency of the simulated bunches was 3.3 MHz while FWHM of bunch signal was 30 ns.

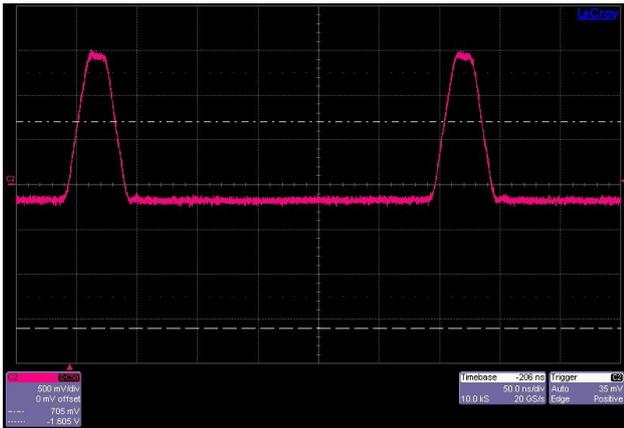


Figure 7: Input signal

Evaluation was done with bunch-by-bunch position results within the dynamic range of 15 dB. The RMS was calculated on the 10000 acquired position samples. Kx and Ky geometrical coefficients were set to 100 mm. Results are presented on Fig. 8.



Figure 8: Position resolution (15 dB dynamic range / FWHM=30ns)

Libera Hadron RF front-end hardware configurability enables processing of various signal shapes and intensities. In the case of lower signal intensities, the RF front-end gain can be reconfigured.

In the case of the following test, the full scale of the instrument was set to 70 mV peak. Same signal shape was used as in previous case. Kx and Ky geometrical coefficients were set to 100 mm. Results are presented on figure 9.

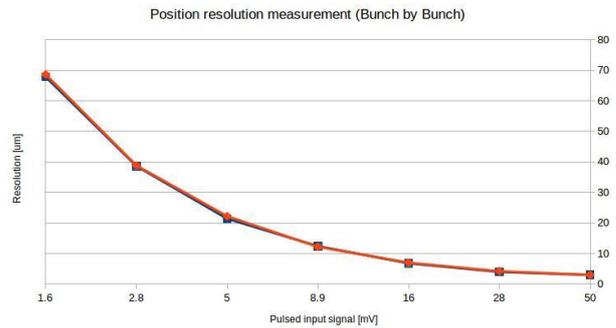


Figure 9: Position resolution (30 dB dynamic range / FWHM=100ns)

EXTERNAL PREAMPLIFIER

External module called Amplifier 110 is intended to reduce big dynamic range of signals in order to enable further processing/measuring. Huge 110 dB (-50 dB to 60 dB) beam dynamic in SIS-100 accelerator can not be coupled directly with the Libera Hadron BPM. External preamplifier mounted close to the sensors is in charge to provide high intensity and low noise signals to Libera Hadron unit. Control data are transferred from the Libera Hadron to the Amplifier 110 by using SPI protocol. The Amplifier 110 can be controlled from the accelerator event system through Libera Hadron or directly from the Libera Hadron unit.

CONCLUSION

One of the crucial requirements for the SIS-100 BPM is the actual bunch-by-bunch measurement resolution under specific conditions, which must be less than 100 μm for each individual bunch. Position resolution close to 7 μm was achieved in the test setup in the laboratory. Extensive testing will be carried out at GSI SIS-18, where beam tests of the Libera Hadron unit will be performed.

The successful collaboration needs to be emphasized between users (GSI - FAIR) and development & manufacturing of the instrument (Instrumentation Technologies).

REFERENCES

- [1] fair-center.eu website: <http://www.fair-center.eu/public/what-is-fair/accelerators.html>
- [2] Instrumentation Technologies, "BPM data acquisition for circular machines", internal document, Solkan (December 2013)
- [3] Instrumentation Technologies, "HBPM project design document", internal document, Solkan (January 2014)